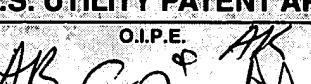
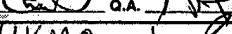




ISSUE CLASSIFICATION						PATENT NUMBER
103	01	037/214	03	01		
U.S. UTILITY PATENT APPLICATION						
 SCANNED CO Q.A.			O.I.P.E. PATENT DATE			
 H.K.M.2						
APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER	
09/960585		430 356	500	1756 2877	LYONS LEE	
APPLICANTS	Fuyuhiko Inoue					
TITLE	Switching type dual wafer stage					
PTO-2040 12/99						

PREPARED AND APPROVED FOR ISSUE

ISSUING CLASSIFICATION

Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> a) The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	(Assistant Examiner)	(Date)			
<input type="checkbox"/> b) The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
	(Primary Examiner)	(Date)		Amount Due	Date Paid
<input type="checkbox"/> c) The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
	(Legal Instruments Examiner)	(Date)			

WARNING:

The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

Form PTO-436A
(Rev. 10/97)

(LABEL AREA)

(FACE)